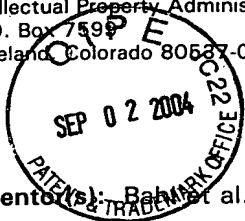


AGILENT TECHNOLOGIES, INC.  
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ATTORNEY DOCKET NO. 10040093-1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Bahmany et al.

Serial No.: 10/817,514

Examiner: TBA

Filing Date: April 2, 2004

Group Art Unit:

Title: Gallium Arsenide Antimonide (GaAsSb)/Indium Phosphide (InP) Heterojunction Bipolar Transistor (HBT) Having Reduced Tunneling Probability

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

This Information Disclosure Statement is submitted:

- (a) ☒ Under 37 CFR 1.97(b).  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first Office action on the merits; whichever occurs last).
- (b) ☐ Under 37 CFR 1.97(c) together with *either* a:  
☐ Statement under CFR 1.97(e), or  
☐ \$180.00 fee under 37 CFR 1.17(p).  
(After the CFR 1.97(b) time period, but before a final action or notice of allowance, whichever occurs first).
- (c) ☐ Under 37 CFR 1.97(d) together with: a  
Statement under 37 CFR 1.97(e), *and*  
\$180.00 fee as set forth in 37 CFR 1.17(p).  
(After a final action or notice of allowance, whichever occurs first, but before payment of the issue fee).

☐ **STATEMENT UNDER 37 CFR 1.97(e)**

The undersigned certifies that:

- ☐ Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement, *or*
- ☐ No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

☐ **PRIOR APPLICATIONS**

References identified with an asterisk (\*) in the enclosed PTO Form 1449, were disclosed in prior Patent Application No. , filed , now U.S. Patent No. , and, as such, copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).

☐ **FOREIGN LANGUAGE DOCUMENTS**

A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56(c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

☐ **FEE AUTHORIZATION**

Please charge to Deposit Account **50-1078** the sum of \_\_\_\_\_. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **50-1078** pursuant to 37 CFR 1.25.

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☐ I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Date of Facsimile:

Typed Name: Lucy J. Lehman

Signature: 

Respectfully submitted,  
Bahl et al.

By 

Michael J. Tempel  
Attorney/Agent for Applicant(s)

Reg. No. 41,344

Date: August 30, 2004

Telephone No. (678) 420-9300



Form PTO-1449	ATTORNEY DOCKET NO. 10040093-1	APPLICATION NO. 10/817,514
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	APPLICANT Bahl et al.	
	FILING DATE April 2, 2004	GROUP TBA
(Use several sheets if necessary)		

REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS		
EXAMINER INITIALS	*	DOCUMENT NUMBER	DATE	NAME

FOREIGN PATENT DOCUMENT					
		DOCUMENT NUMBER	DATE	NAME	TRANSLATION YES NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)	
B1	InGaAs/InP Double-Heterostructure Bipolar Transistors With Near -Ideal $\beta$ Versus $I_c$ Characteristic, Nottenburg et al., IEEE Electron Device Letters, Vol. EDL-7, No. 11, pp 643-645, 1986

EXAMINER	DATE CONSIDERED
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